

Fig. 4

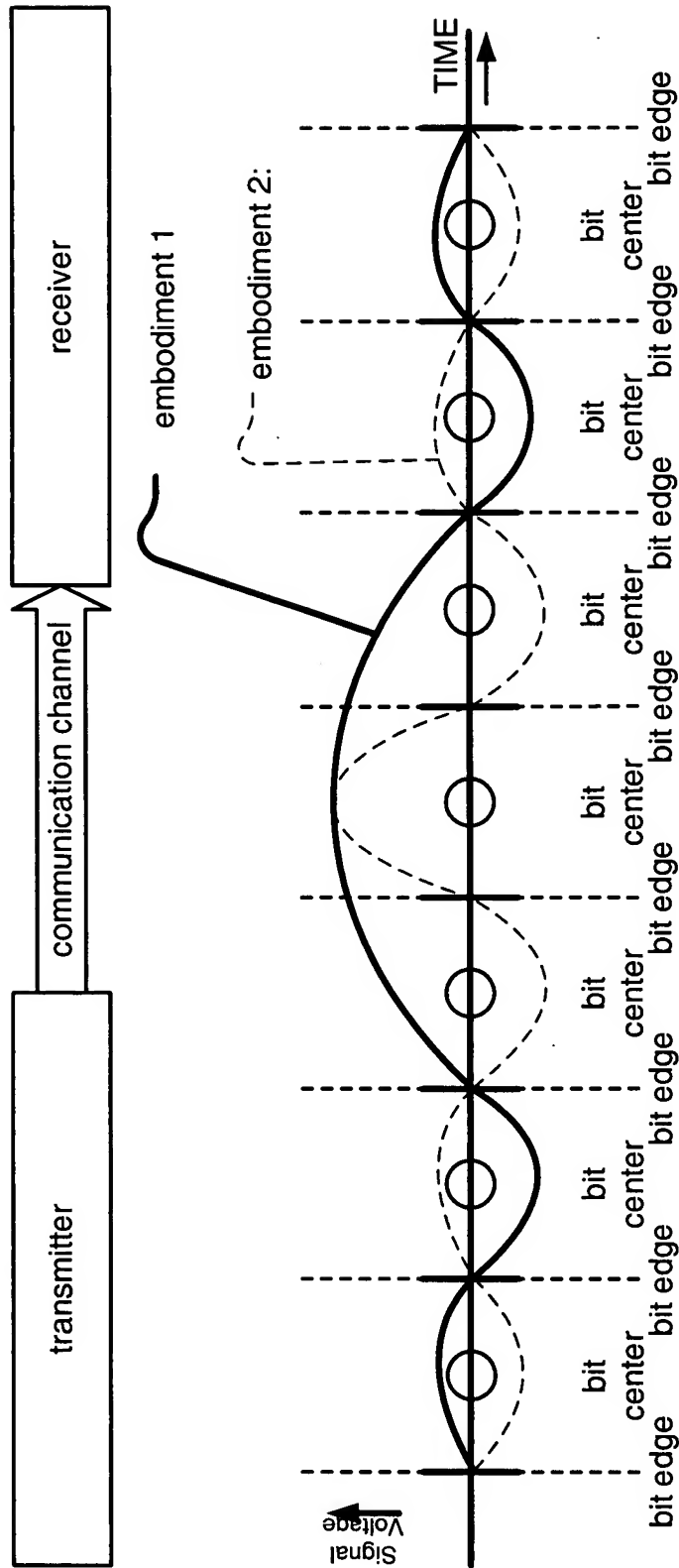
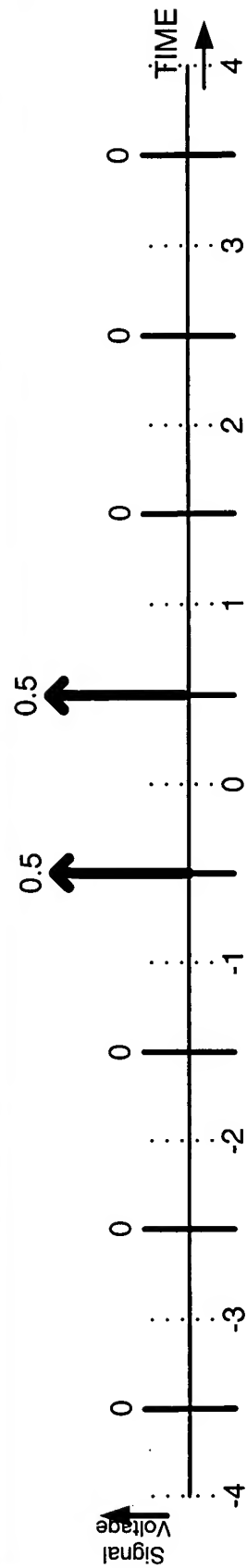
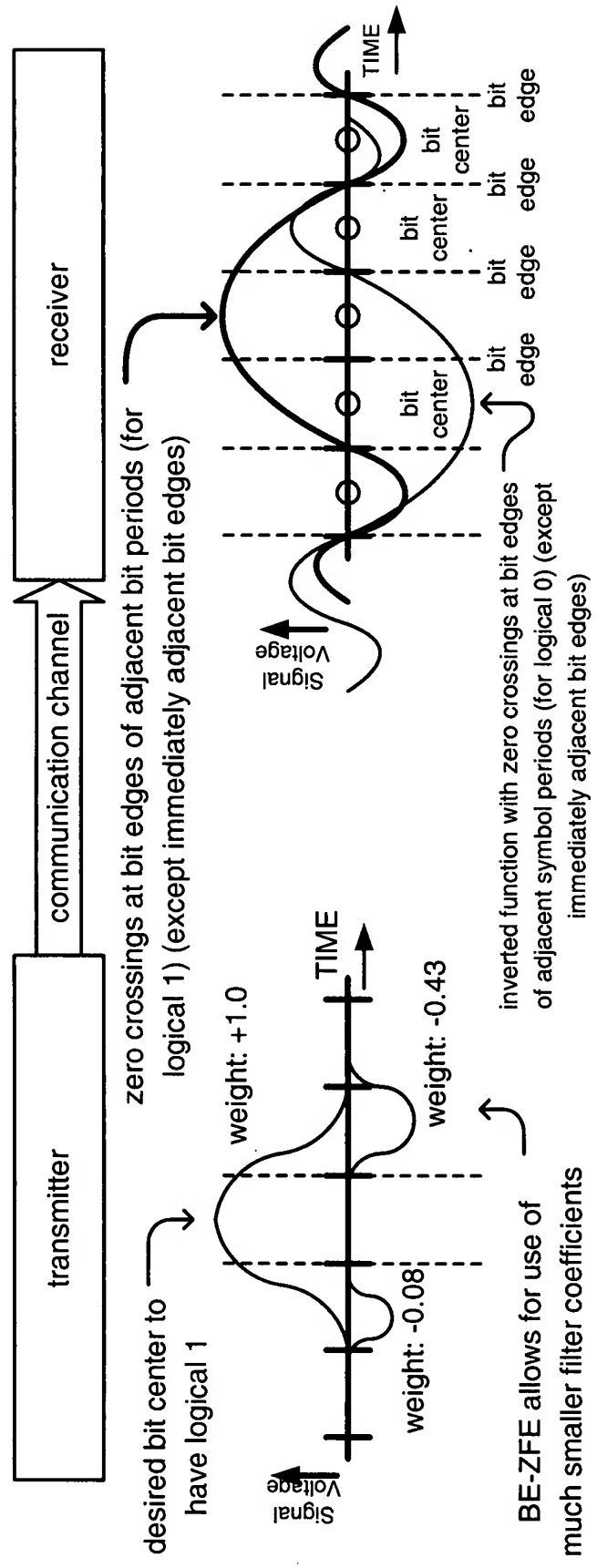


Fig. 5A



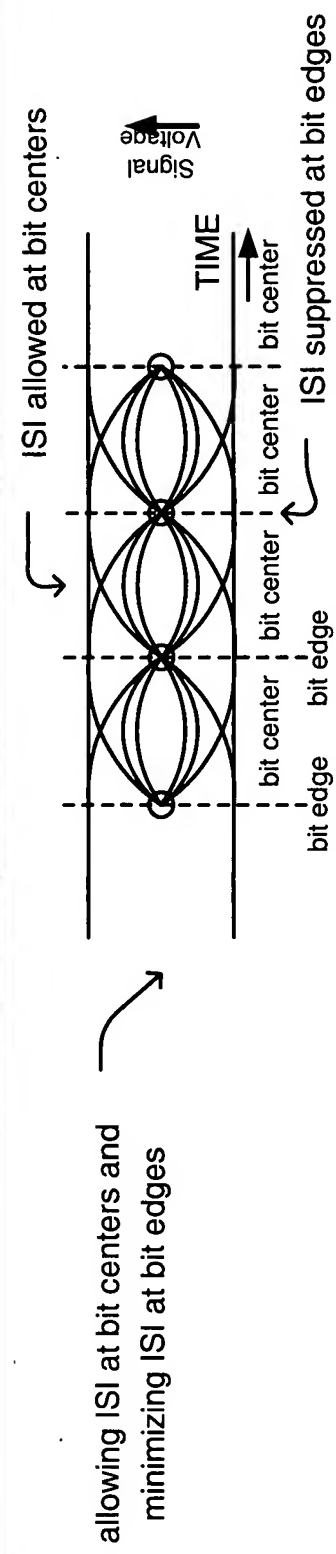
mapping of embodiment 1: BE-ZFE (takes pulse response values at bit edges and drives error to zero using mapping)

Fig. 5B



BE-ZFE (Bit-Edge Zero Forcing Equalizer) signal shaping approach to send logical 1

Fig. 6A



eye diagram of BE-ZFE signal shaping approach showing relative location of ISI (Inter-Symbol Interference)

Fig. 6B